

ABSTRACT OF DISCLOSURE

The present invention relates to method and apparatus for suppressing a DC component of coded sequence with no additional bit for suppressing a DC component, and for decoding the coded sequence. The present modulating method includes modulating the source data to a coded data based on at least one mapping table, the mapping table including a first table and a second table containing coded data corresponding to the source data, the first table being a 2/3 table containing 3-bit coded data for 2-bit source data, and the second table being a 4/6 table containing 6-bit coded data for 4-bit source data, wherein one of the first table and the second table is selected according to a bit sequence of the source data; and recording the coded data onto the optical recording medium.